

Course Synopses:

<b>Course Code</b>	PHU 4301	<b>Level</b>	4			
<b>Course Title</b>	Electronics					
<b>Credit value</b>	03					
<b>Core/Optional</b>	Optional					
<b>Prerequisites</b>	PHU 4303					
<b>Hourly breakdown</b>	<b>Theory</b>		<b>Practical hours</b>	<b>Independent Learning</b>	<b>Assessments</b>	<b>Total hrs</b>
	22 Sessions X 2 = 44 hrs	4 DS x 3 hrs = 12 hrs	Lab /field 9x3 = 27 hrs	<ul style="list-style-type: none"> <li>▪ 22 Sessions (x 3) = 66 hrs</li> <li>▪ Online learning resources= 05 hrs</li> </ul>	Continuous Assessments ( 1 hr) x 2 = 2 hrs	156
<b>Course Aim/s.</b>	Provide knowledge and skill required to design an implement an electronic circuit to solve a given problem and to analyse an existing circuit.					
<b>POs addressed by course</b>	<p><b>PO1: Knowledge</b> - Explain the fundamental, principles and broader knowledge pertaining to the chosen science disciplines offered for the BSc degree.</p> <p><b>PO2 Practical Knowledge and Application</b> - Acquire competency in practical skills and the necessary knowledge to appropriately use these skills.</p> <p><b>PO3: Communication</b> - Communicate reliably, efficiently and effectively to present information, ideas and concepts to the scientific community as well as to the wider society.</p> <p><b>PO4: Individual Work, Team Work and Leadership</b> - Function effectively as an individual, and as a team member, sharing work and experiences, leading and managing assigned tasks to completion on time, demonstrating leadership to address situations in diverse and multi-disciplinary environments in day to day life.</p> <p><b>PO5: Creativity and Problem Solving</b> - Identify problems and argue out and analyze such problems using qualitative and/or quantitative practical approaches in scientific methodology to provide valid conclusions.</p> <p><b>PO6: Adaptability and Flexibility</b> - Develop appropriate strategies to adapt to changing environments.</p> <p><b>PO7: Information and Communication Technology Literate:</b> Effectively use ICT skills for numerical and statistical analysis keeping up to date with knowledge and skills.</p> <p><b>PO9: Lifelong Learning:</b> Foresee new trends and recognize their impact, and update knowledge and develop new skills to meet future changes and challenges</p>					
<b>Course Learning Outcomes (CLO)</b>	<p>At the completion of this course student will be able to:</p> <p><b>CLO1</b> Explain and apply fundamental principles in semiconductors, analogue electronics and digital electronics (PLO 1, PLO 2)</p> <p><b>CLO 2</b> Analyze a problem logically and come up with a suitable electronic circuit to solve it. (PLO 3, PLO 5 PLO 6, PLO9)</p> <p><b>CLO 3</b> Develop skill to use electronics laboratory equipment's and implement circuits (PLO 2, PLO 4)</p> <p><b>CLO 4</b> Ability to learn new concepts and technologies in electronic via internet or other mediums (PLO 3, PLO 7, PLO 9)</p> <p><b>CLO 5</b> Ability to works as an individual and as a group in laboratory sessions. (PLO 3, PLO 4, PLO 5, PLO 6)</p>					
<b>Content (Main topics, sub topics)</b>	<p><b>Basic Concepts</b>, Modern applications , Electrical vs electronics, Organization of the book, Performance table, Current, Conduction mechanism of current, Voltage, Current voltage sources, Series and parallel, Ohms law, Kirchhoff's law, (Norton + Thevinings), Types of conductors, Atomic level description Energy bands, Doping , N type, P type, PN junction, depletion region, Forward and reverse bias, Characteristic curve, Ideal vs real diode, <b>Analog Electronics</b>, Diode calculations, Diode applications,, Zenner diode structure, applications and calculations, LEDs, photo diodes, tunnel diodes, varactors, BJT vs FET, BJT structure, Notations, Configurations, Behavior, Characteristic curves, Calculations of Ib, IC, VCE, Load line, Transistor biasing , Q point and amplifier classes, Push pull amplifier, Transistor as a switch, Structure and characteristics, Calculations and applications of FETs, Structure and applications, Parameters, Ideal vs real, Golden rules, inverting , non-inverting, negative and positive feedback, Calculations of amplifications, Filters, Peak detectors, Math operators, g , h parameter modeling, <b>Digital Electronics</b>, Analogue vs digital, Gates, Boolean algebra, K maps, truth tables, Adders, multiplexers, Design a circuit, Flip flops, Counters, Registers, Design a circuit, Importance and applications, Resolution and sampling frequency, ADC circuits, DAC circuits, Practical problems to solve (Dark detector , Sun tracker, Binary clock, etc. ), <b>laboratory sessions</b>, Component identification, Instrument usage (Oscilloscope, signal generator..etc.), Soldering skills, Diode characteristics and applications, transistor characteristics and applications, Op-amps, Combinational and Sequential circuits, Design and test a circuit.</p>					

<b>Teaching Learning methods (TL)</b>	<ul style="list-style-type: none"> <li>• Self-Learning/Independent learning of Self-study <ul style="list-style-type: none"> <li>• Instructional Material (IL)</li> <li>• Online Activities (OL)</li> </ul> </li> <li>• Compulsory contact sessions <ul style="list-style-type: none"> <li>• Practical Sessions (PR)</li> <li>• Reports (RE);</li> </ul> </li> <li>• Non-compulsory contact sessions <ul style="list-style-type: none"> <li>• Day Schools (DS)</li> </ul> </li> </ul>	
<b>Assessment strategy</b>	Overall Continuous Assessment Mark (OCAM): 40%	Final Assessment: 60%
	Details: 36 % of Best CA + 24 % of the second best (1 hr each) + 40% PM	Final Evaluation Theory: 100 (2 hrs) %
	Overall mark = 40 % OCAM +6 0 % Final Examination	
<b>Recommended Readings:</b>	<p>[1] Paul Horowitz and Winfield Hill, <i>Art of Electronics</i>, (3rd Edition or Later) Cambridge University Press</p> <p>[2] Paul Scherz, <i>Practical Electronics for Inventors by</i>, McGraw-Hill Publications</p>	